



Erasmus+

Partner Search Form

INSTITUTION/CONTACT PERSON:			
Last Name:	KIRAY	First Name:	VEDAT
Institution	Turgut Ozal University		
Role in the Institution	Faculty member (Associate professor)		
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INFORMATION ABOUT THE PLANNED PROJECT:		
Type of the Project Idea	Erasmus+ KA2: Capacity Building Higher Education in Partner Countries	
Discipline/Academic Field	Engineering – Electronics and Computer	
Institution's preferable role in the project (Applicant or Partner)	Partner	
Which countries are about to be involved	Erasmus+ Programme Countries	Erasmus+ Programme Countries
	Erasmus+ Partner Countries	Kyrgyzstan and Tajikistan
Working language of the project consortium/project application	English	
Duration of the Project	3 years	

PROJECT DESCRIPTION:
Objectives The main objective is to help higher education institutions (HEI) that has got departments related to electronics and computer in the Kyrgyzstan and Tajikistan. Sub-goals: • To increase the role of HEIs in both Kyrgyzstan and Tajikistan • To expand the FPGA (Field Programmable Gate Arrays) technology and its applications in Kyrgyzstan and Tajikistan through the HEIs

- To increase experiences of faculty members and education quality of HEIs in the Kyrgyzstan and Tajikistan
- To Support cooperation between universities and industry in both Kyrgyzstan and Tajikistan
- To increase employment in both Kyrgyzstan and Tajikistan
- To give opportunity to electronics and computer engineers/candidates to prove themselves
- To Improve the EU's relations with Kyrgyzstan and Tajikistan

Activities

- Organizing Intensive seminars and workshops for HEIs' faculty members
- Organizing Basic Electronics, Digital Electronics and FPGA based Design seminar series for engineer candidates and engineers
- Visiting the partner Universities of Project for Assessment meetings and design competitions

Results

- More active HEIs in helping the engineer candidates and engineers
- Qualified engineers
- Better cooperation between HEIs and Industry in Both Kyrgyzstan and Tajikistan
- Teaching and learning experiences and Documentation related to FPGA applications and seminars

We are searching for:

Types of Institutions	Educational or research establishments
Country/Region	Any Programme country
Institutions' Profiles	Knowledge of FPGA technology
Other Relevant Information	The applicant already have contacts in the region

We would be extremely grateful for a quick response since the deadline for the project is close (February 10th). We need to update the project application as soon as we agree with our prospective partners.